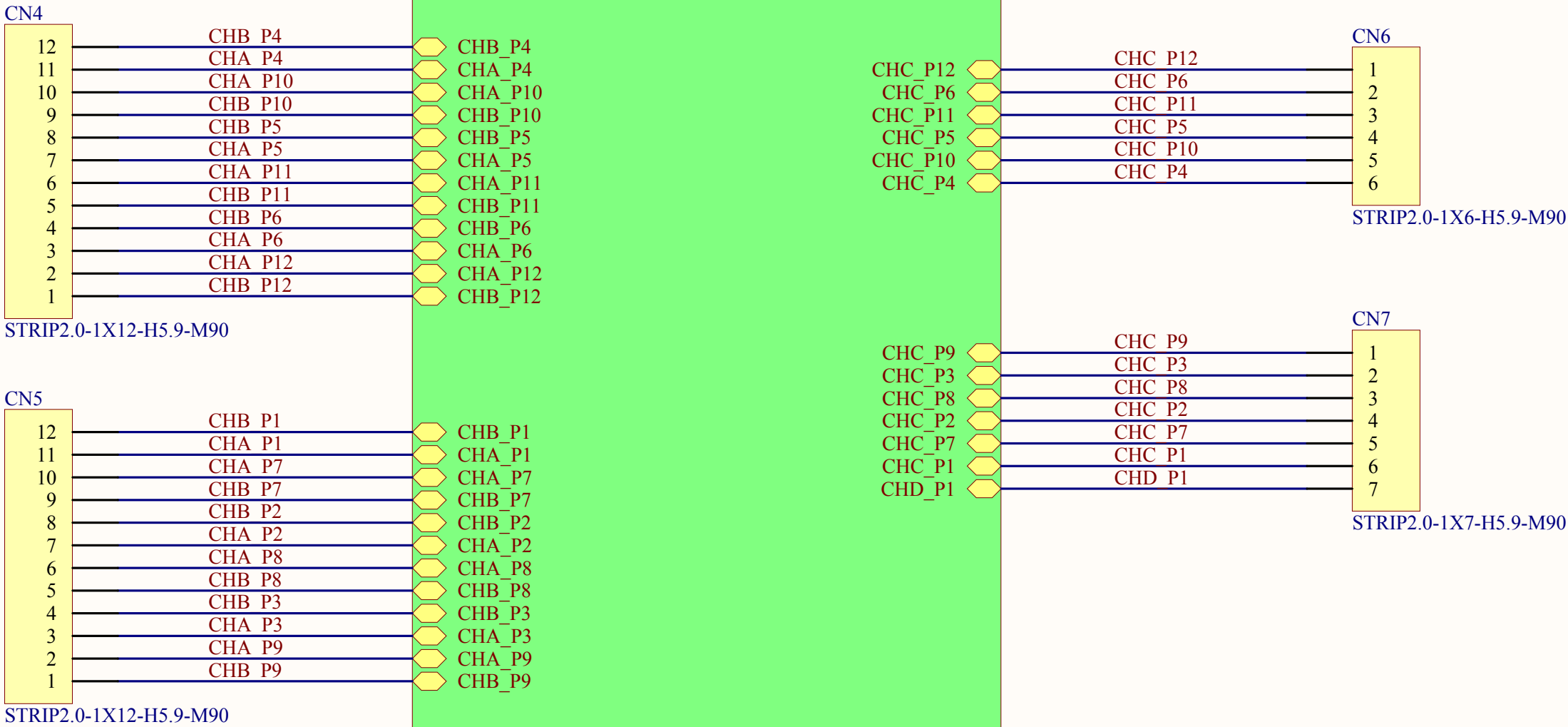


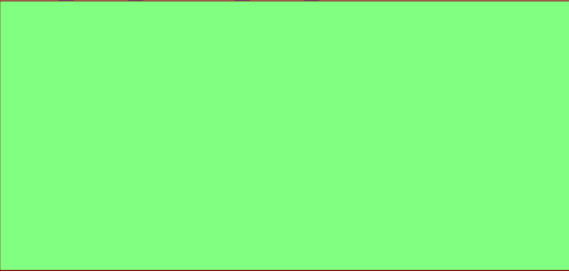
Rev.	Date	Author	Notes / Modifications

NOTES:

U_UPC_CPU_ATM32_LEO_CPU
UPC_CPU_ATM32_LEO_CPU.SchDoc



U_UPC_CPU_ATM32_LEO ALIM
UPC_CPU_ATM32_LEO ALIM.SchDoc



G5
T

G6
N

FD1
FIDUCIAL

FD2
FIDUCIAL

FD3
FIDUCIAL

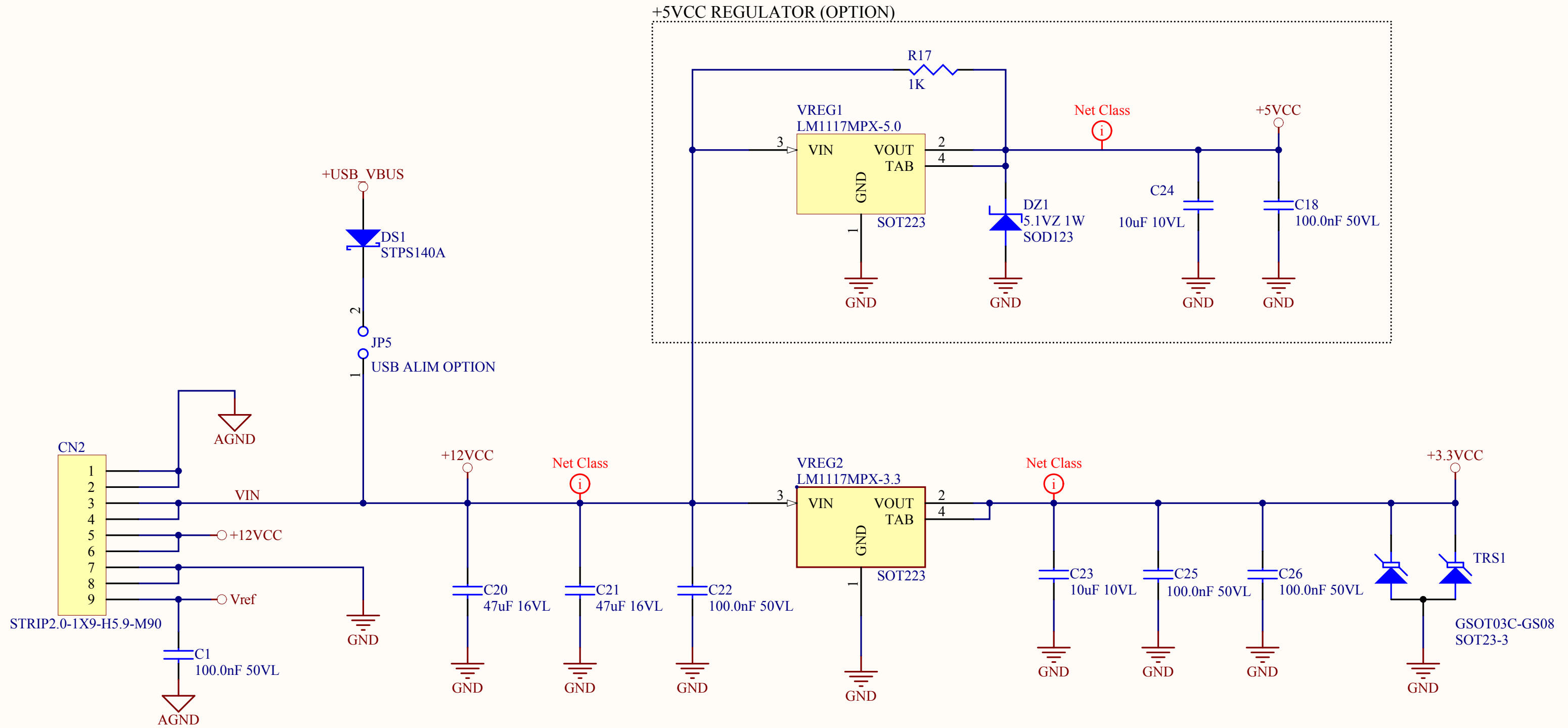
G1
Logo TOP

G2
Logo BOTTOM

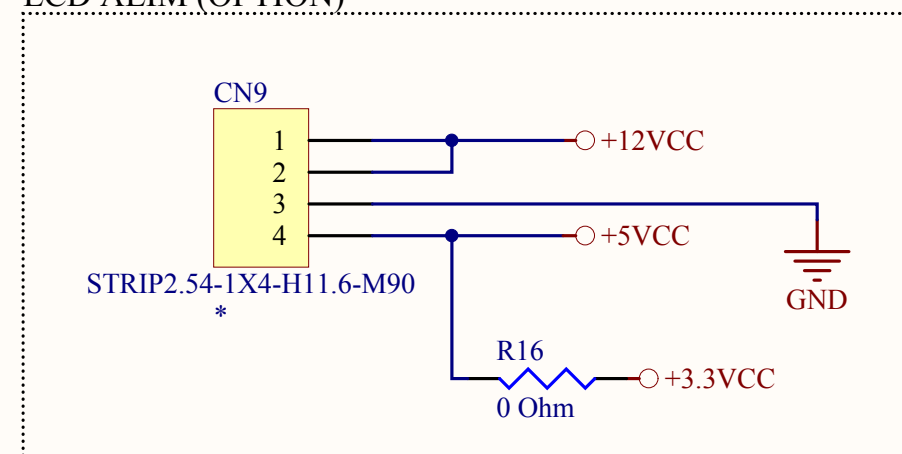
G3
1-LC
2-Layers Label



Project UPC_CPU_ATM32_LEO		Ver. 1.0
Designed by: Seletronica Team	Revised by: Seletronica Team	Rev. A
Document number 0414 - UPC_CPU_ATM32_LEO_V1.SchDoc		Size A4
Date: 08/07/2014		Sheet 2 of 4

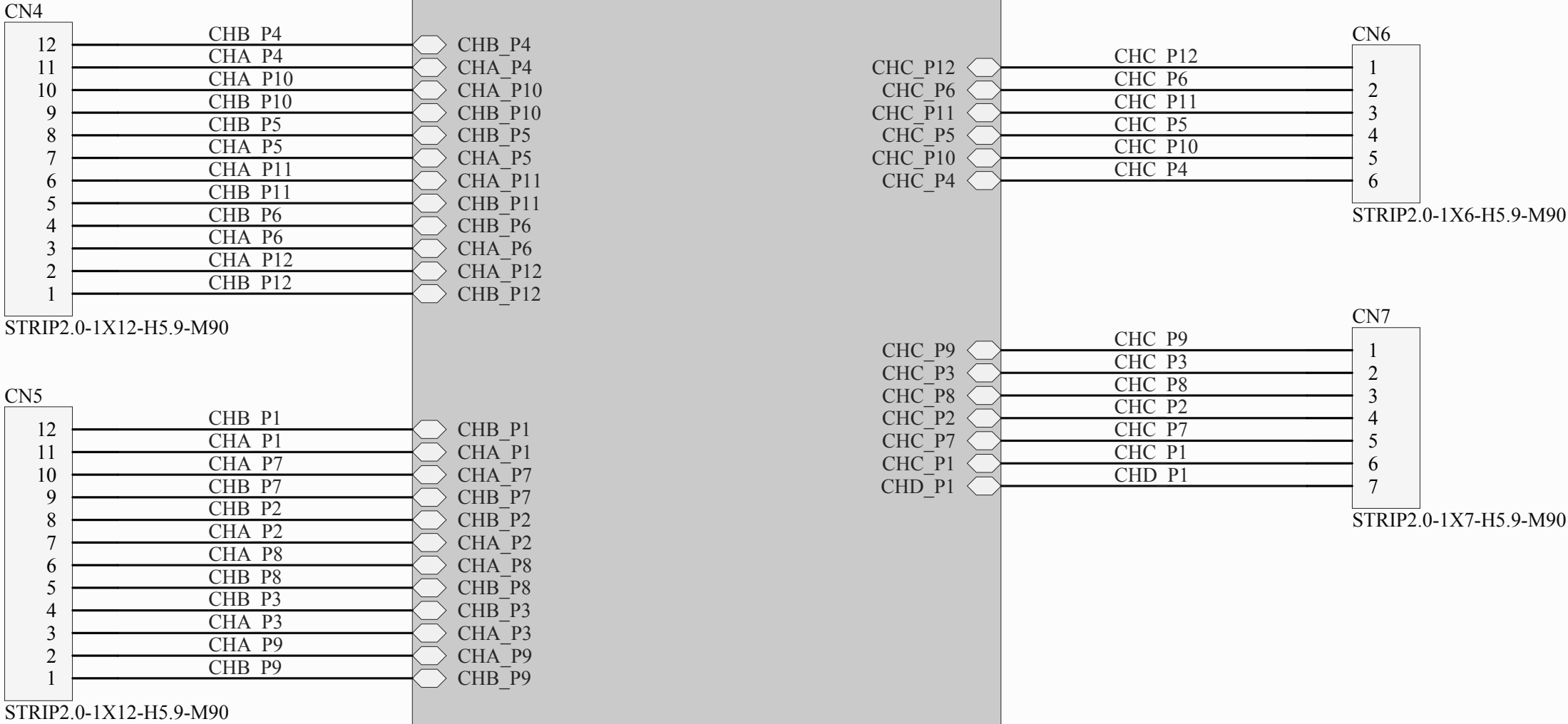


LCD ALIM (OPTION)



Project		UPC_CPU_ATM32_LEO	Ver. 1.0
Designed by:		Seletronica Team	Rev. A
Revised by:		Seletronica Team	Size A4
Document number		0414 - UPC_CPU_ATM32_LEO_ALIM.SchDoc	
Date: 08/07/2014		Sheet * of *	

U_UPC_CPU_ATM32_LEO_CPU
UPC_CPU_ATM32_LEO_CPU.SchDoc



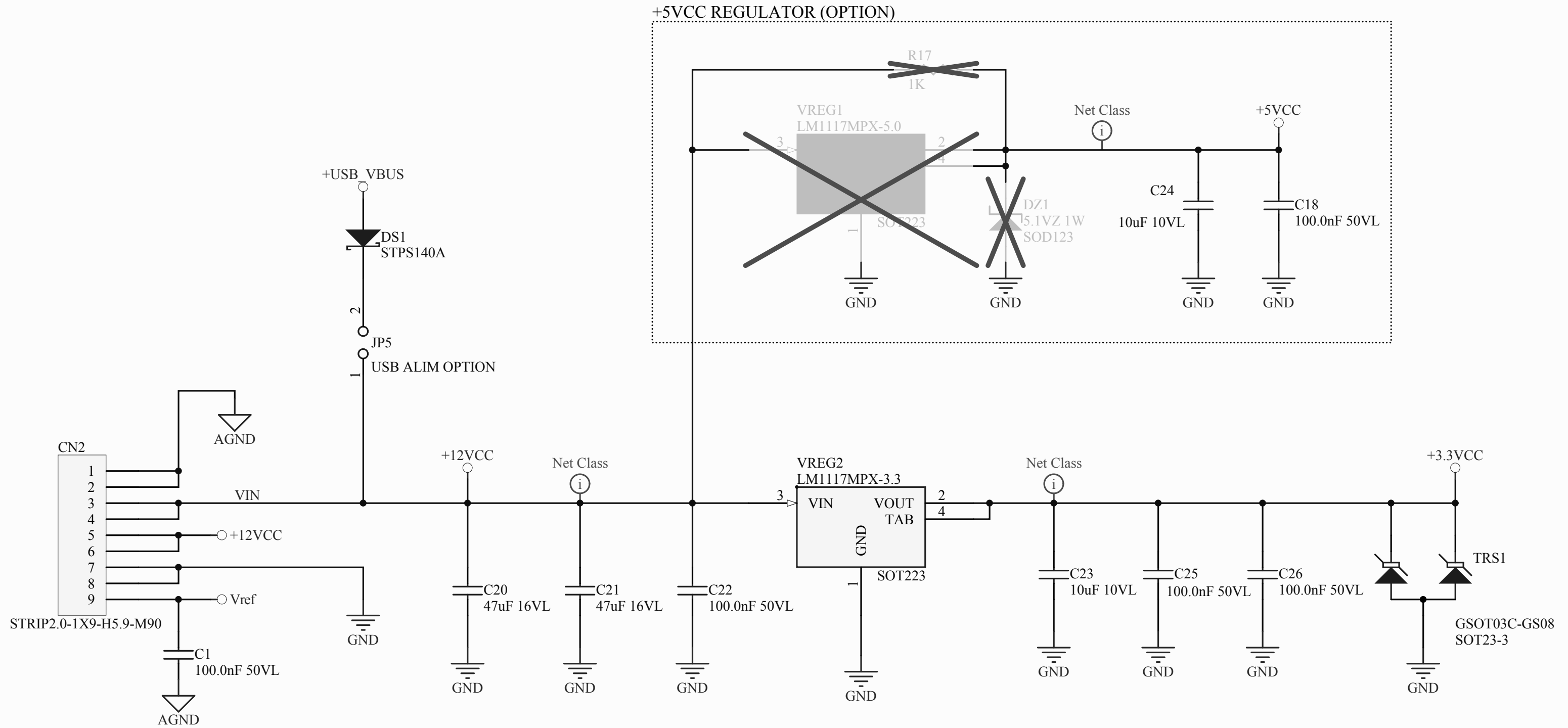
U_UPC_CPU_ATM32_LEO ALIM
UPC_CPU_ATM32_LEO ALIM.SchDoc



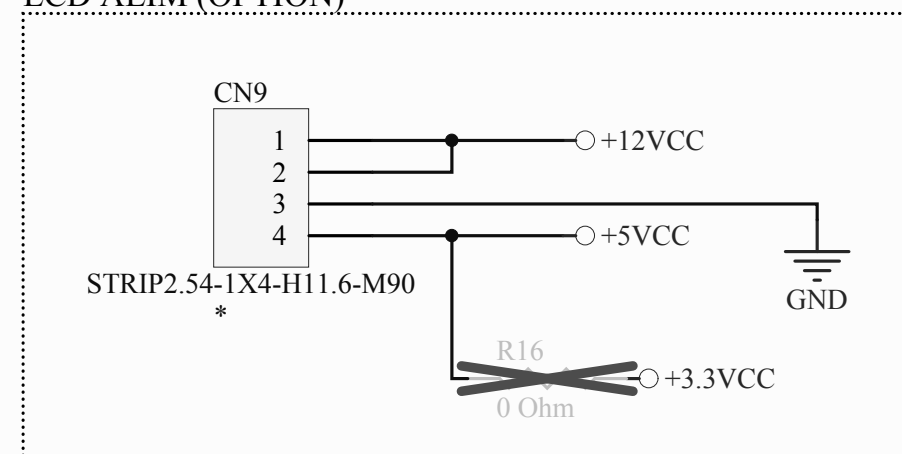
G5 T G6 N



Project UPC_CPU_ATM32_LEO		Ver. 1.0
Designed by: Seletronica Team	Revised by: Seletronica Team	Rev. A
Document number 0414 - UPC_CPU_ATM32_LEO_V1.SchDoc		Size A4
Date: 08/07/2014		Sheet 2 of 4



LCD ALIM (OPTION)



Project		UPC_CPU_ATM32_LEO	Ver. 1.0
Designed by:		Seletronica Team	Rev. A
Revised by:		Seletronica Team	
Document number			Size
0414 - UPC_CPU_ATM32_LEO_ALIM.SchDoc			A4
Date: 08/07/2014			Sheet * of *